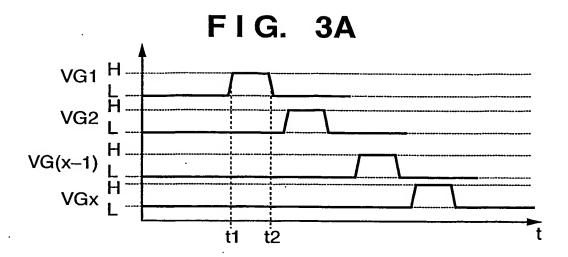
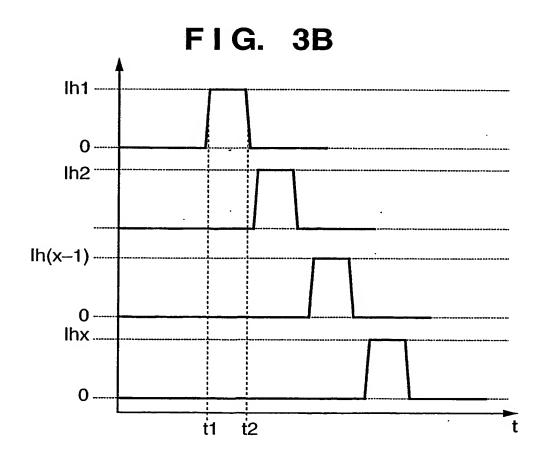


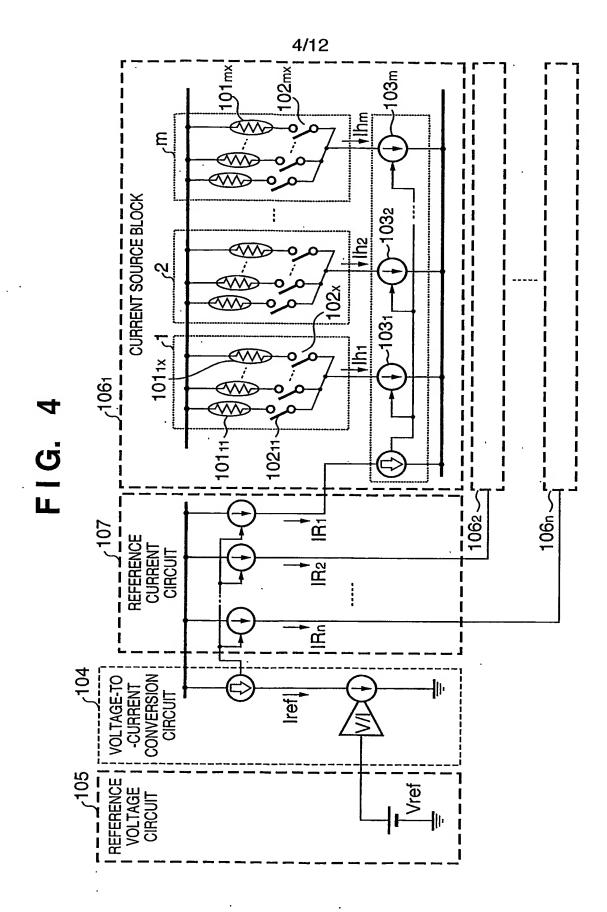
2/12

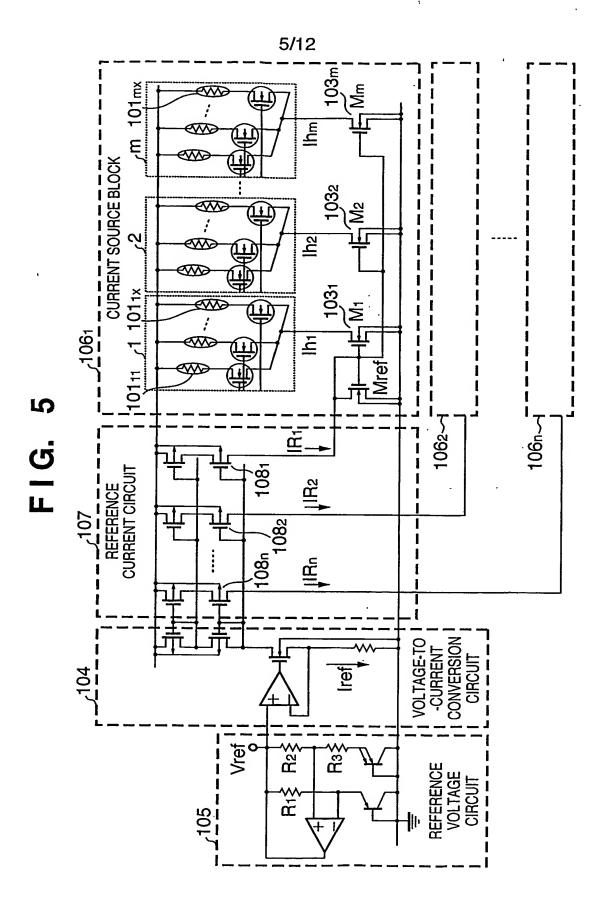
<mark>↓</mark>lhm 103m 100 **CURRENT SOURCE BLOCK** ↓lh₁ VG× ξ VG1 VG2 FIG. 2 VOLTAGE-TO-CURRENT
CONVERSION CIRCUIT Mref lref \$R21 % % 83. , Vef REFERENCE VOLTAGE CIRCUIT ₩ ₩

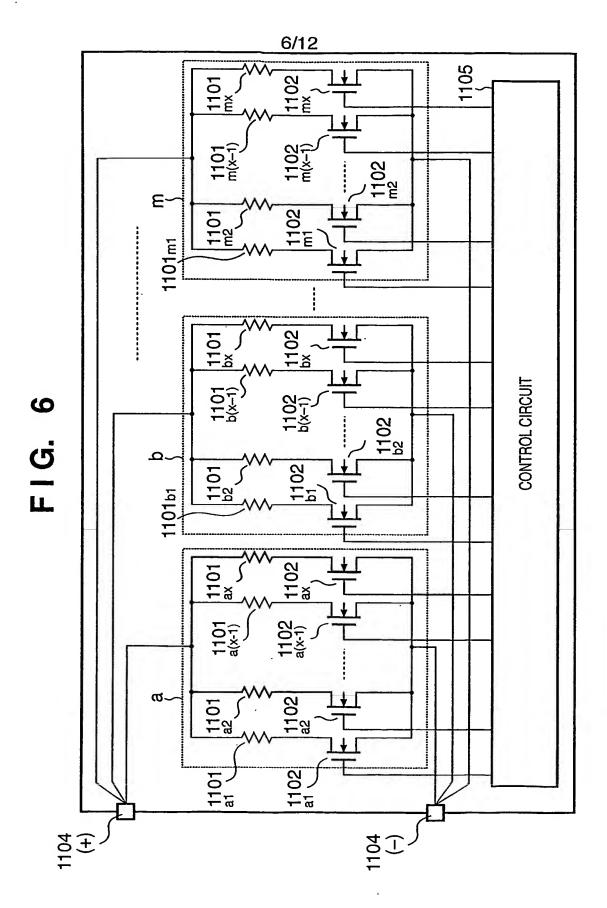
3/12



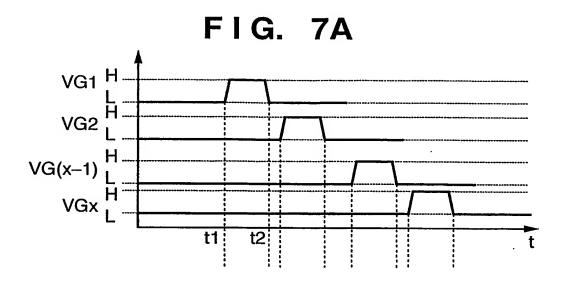


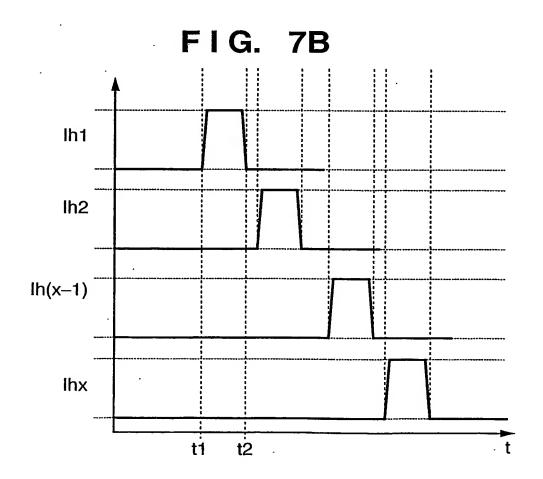




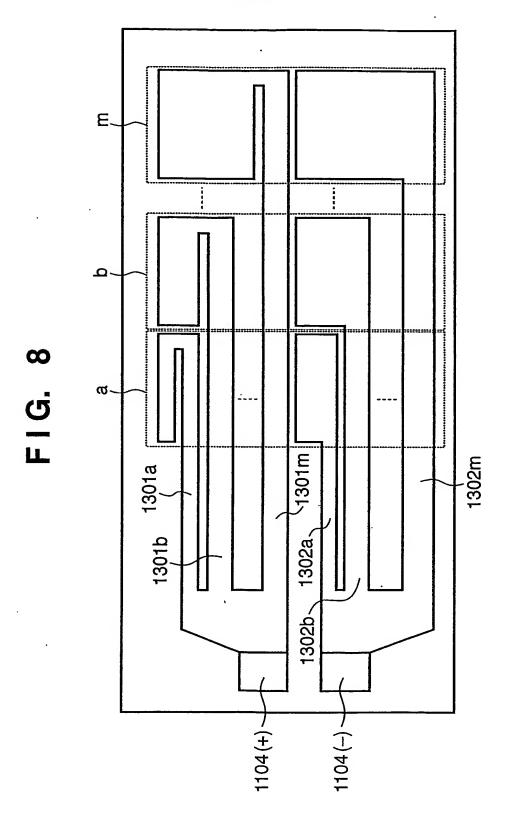


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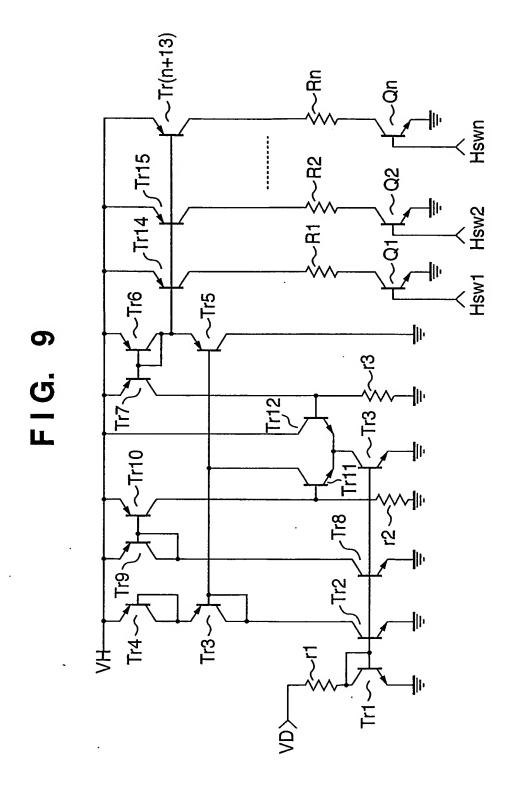




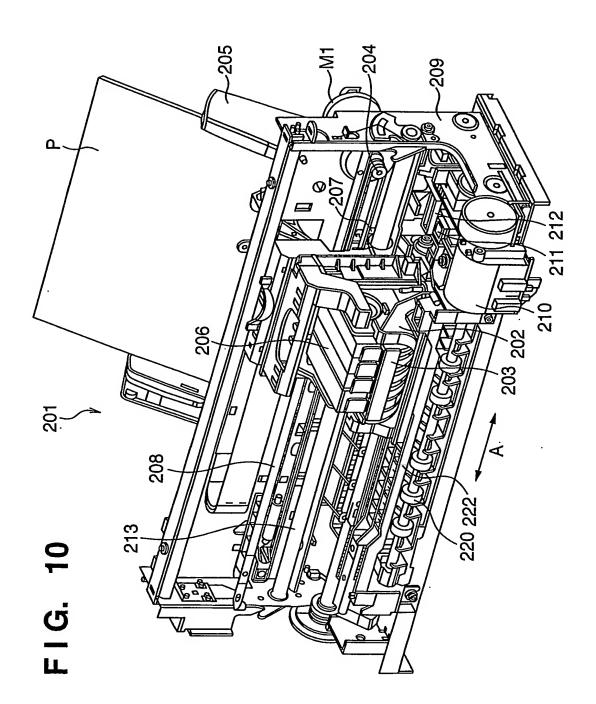
8/12



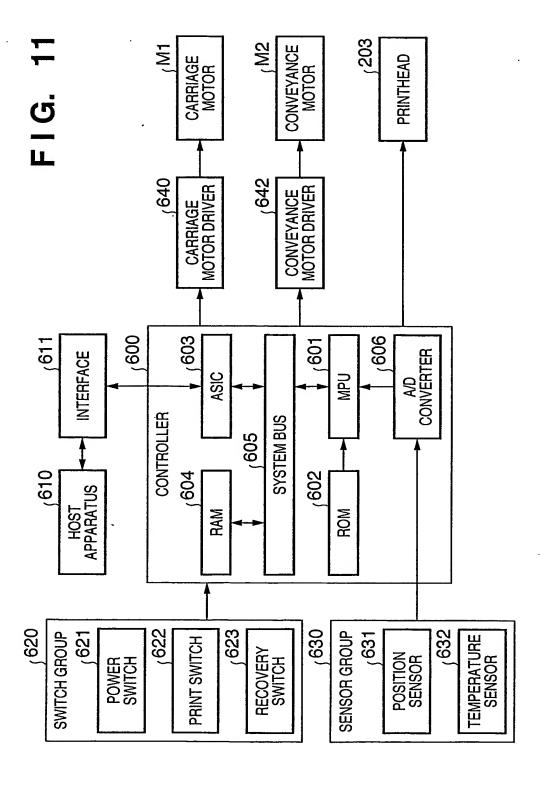
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FIG. 12

